	Application No.	Applicant(s)
	10/679,097	PUGLIESE ET AL.
Notice of Allowability	Examiner	Art Unit
	Raquel Y. Gordon	2853
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>Preliminary Amendment filed 10/3/2003</u> .		
2. The allowed claim(s) is/are <u>1-28</u> .		
3. The drawings filed on <u>03 October 2003</u> are accepted by the Examiner.		
4.		
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material) 6. ☐ Interview Summa Paper No./Mail D 3/08), 7. ☐ Examiner's Amen	Date .

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Acknowledgment of Receipt of Preliminary Amendment

The preliminary amendment filed 10/3/2003 had been received and entered.

Reasons for allowance

The following is a statement of reasons for allowance:

Upon consideration, the inclusion of the claimed substrate and method of forming thereof, as claimed in the combination, is not found in the prior art. This difference is considered patentable over the prior art and reflects an improvement in the field of endeavor since the following apparatus and method claims limitations reflect an improvement in the field of endeavor and over the prior art of record.

The instant application is a divisional of Serial No. 09/772752. With respect to the independent claims, the primary reason for allowance is, the prior art does not recite the claim combination which includes:

A method of forming a slotted substrate, the method comprising: depositing a thin film over a substrate, wherein the thin film contains plural layers including at least an insulating dielectric barrier layer, an interdielectric thin film layer, a resistive layer and a metal conductive layer; forming a slot in the substrate through a slot region that extends through the substrate and the plural layers; and placing the plural layers in a predefined deposit order over the substrate so that a chip count in a shelf surrounding the slot is minimized when the slot is formed in the substrate through the slot region (claim 1);

A method of forming a slotted substrate, the method comprising: depositing at least four plural thin film layers over a substrate, wherein one of the plural thin film layers is a metal thin film layer, one of the thin film layers is an insulating dielectric barrier layer, one of the thin film layers is an interdielectric thin film layer and one of the thin film layers is a resistive layer; and minimizing a chip count in a shelf surrounding a slot defining an area of the slotted substrate by layering the plural thin film layers in a predefined deposit order over the substrate before forming the slot in the substrate through a slot region that extends through the substrate and the plural thing film layers (claim 12);

A method of forming a slot in a substrate comprising: depositing plural thin film layers over a substrate wherein one of the layers in a ductile thin film layer, one of the thin film layers is an insulating dielectric barrier layer, one of the thin film layers is an interdielectric thing film

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layer and one of the thin film layers is a resistive layer; and extending the slot through the ductile thin film layer and the substrate defined by a slot region and layering the plural thin film layers in a predefined deposit order to minimize a chip count in a shelf surrounding the slot. (claim 13);

A coated substrate for a center feed printhead comprising: means for depositing at least four plural thin film layers over a substrate, wherein one of the plural thin film layers is a metal thin film layer, one of the thin film layers is an insulating dielectric barrier layer, one of the thin film layers is an interdielectric thing film layer and one of the thin film layers is a resistive layer; means for forming the slot in a substrate through a slot region that extends through the substrate and thin film; and means for minimizing a chip count in a shelf surrounding a slot defining an area of the slotted substrate by layering the plural thin film layers in t a predefined deposit order over the substrate before forming the slot in the substrate through a slot region that extends through the substrate and the plural thing film layers (claim 20);

A coated substrate for a center feed printhead comprising: a substrate; a thin film applied over the substrate, wherein the thin film contains plural layers including at least an insulating dielectric barrier layer; interdielectric thin film layer, a resistive layer and a metal conductive layer; and a slot region extending through the substrate and the thin film, that a chip count in a self surrounding the slot region is minimized when a slot is formed in the substrate through the slot region (claim 21).

The instant application is a divisional of Serial No. 09/772752, filed 9/21/2001, now Patent No. 6648732. An involuntary restriction requirement precludes Patent No. 6648732 from being applied as a prior art teaching.

Further, an exhaustive search has revealed no prior art to disprove novelty or nonobviousness of the claimed invention.

Hence, the independent claim is now deemed to be allowable. The dependent claims are allowable since they depend from allowed base claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact Information

Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Raquel Y. Gordon, whose telephone number is (571) 272-2145. The Examiner can normally be reached on M-F 8:30-5:00.

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If attempts to reach the examiner by telephone are unsuccessful, the Examiner's supervisor, Stephen Meier can be reached on (571) 272-2149. A fax number is available upon request.

Any inquiry of a general nature or relating to the status of this application or proceeding may be directed to the Examiner or Supervisor.

> Primary Examiner Art Unit 2853

October 8, 2004